

FIG. 1

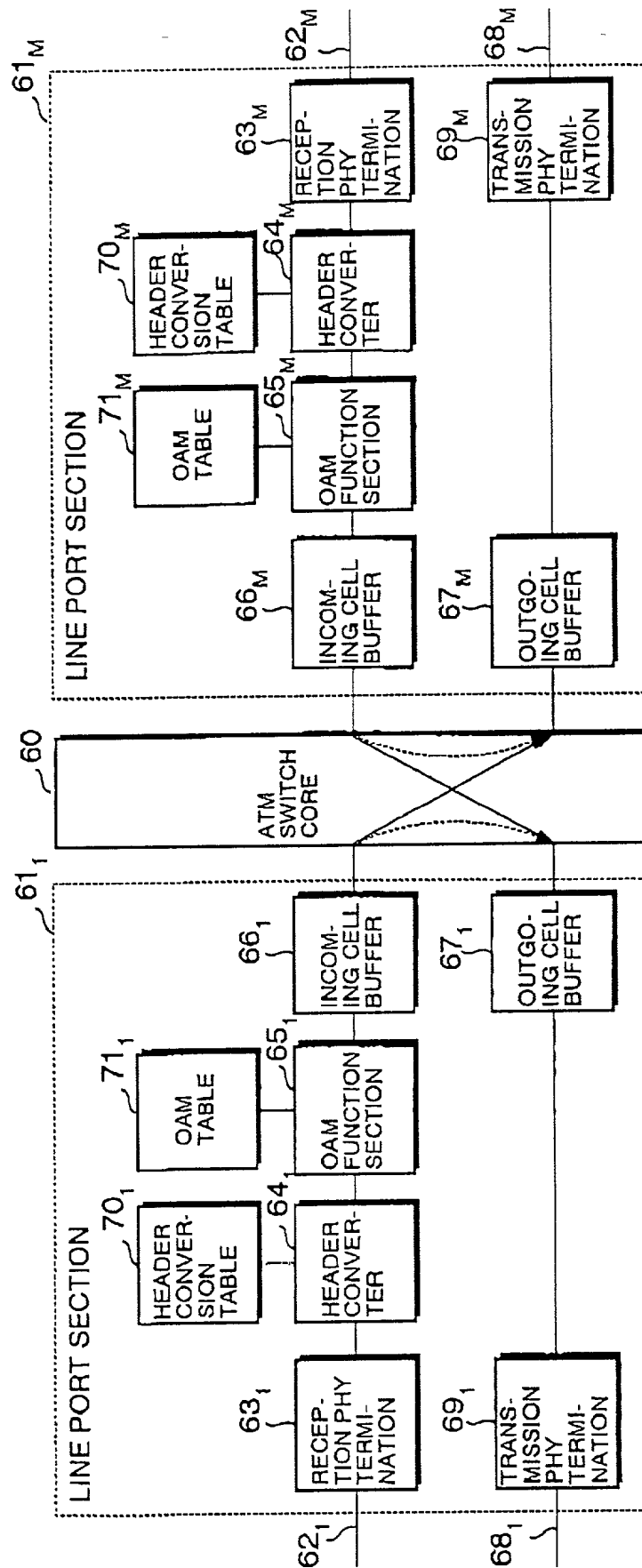


FIG. 2

HEADER CONVERSION TABLE

73	74	75	76	77
VPI / VCI	NEW VPI / VCI	SW OUTPUT PORT NO.	F4 FLOW END POINT	F5 FLOW END POINT
VPI / VCI	NEW VPI / VCI	SW OUTPUT PORT NO.	F4 FLOW END POINT	F5 FLOW END POINT
⋮	⋮	⋮	⋮	⋮
VPI / VCI	NEW VPI / VCI	SW OUTPUT PORT NO.	F4 FLOW END POINT	F5 FLOW END POINT

FIG. 3

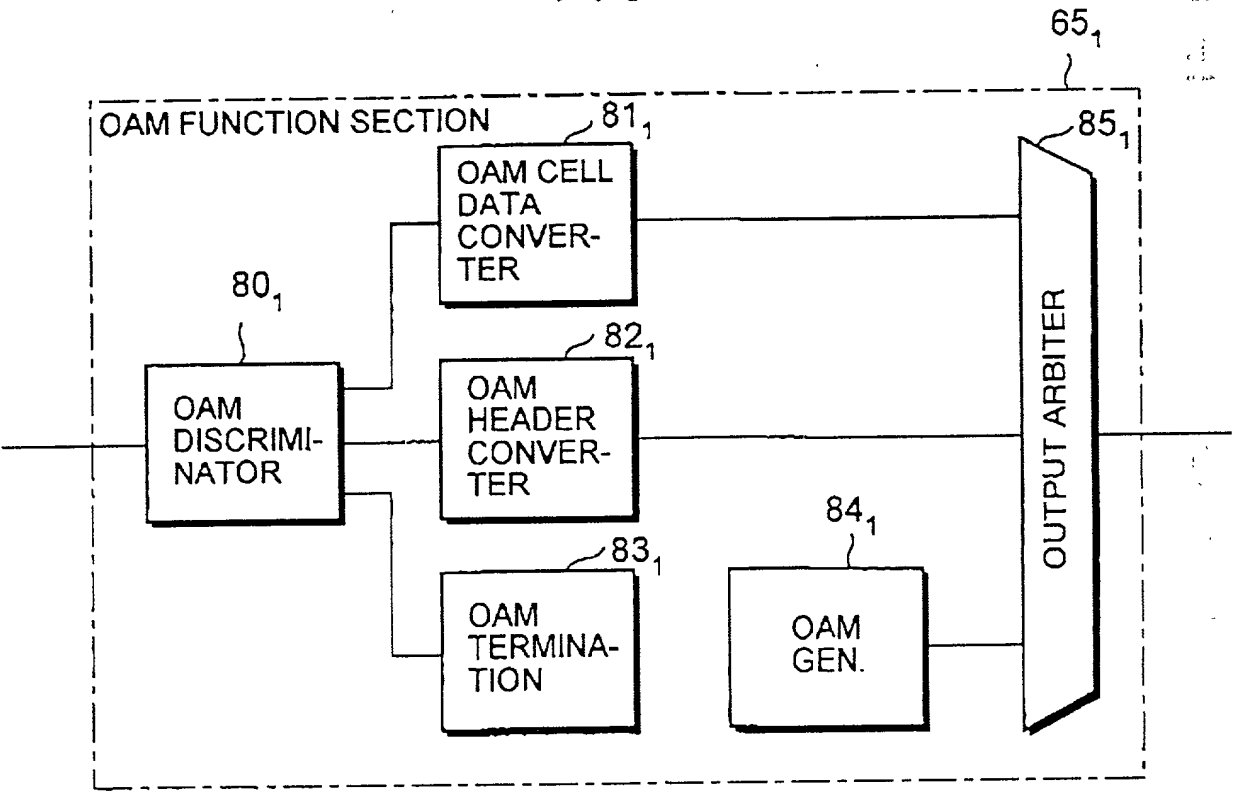


FIG. 4

OAM TABLE

90	91	92	93	94	95
VPI/VC1	AIS	RDI	DEF	SW OUTPUT PORT NO.	NEW VPI/VC1
VPI/VC1	AIS	RDI	DEF	SW OUTPUT PORT NO.	NEW VPI/VC1
⋮	⋮	⋮	⋮	⋮	⋮
VPI/VC1	AIS	RDI	DEF	SW OUTPUT PORT NO.	NEW VPI/VC1

FIG. 5

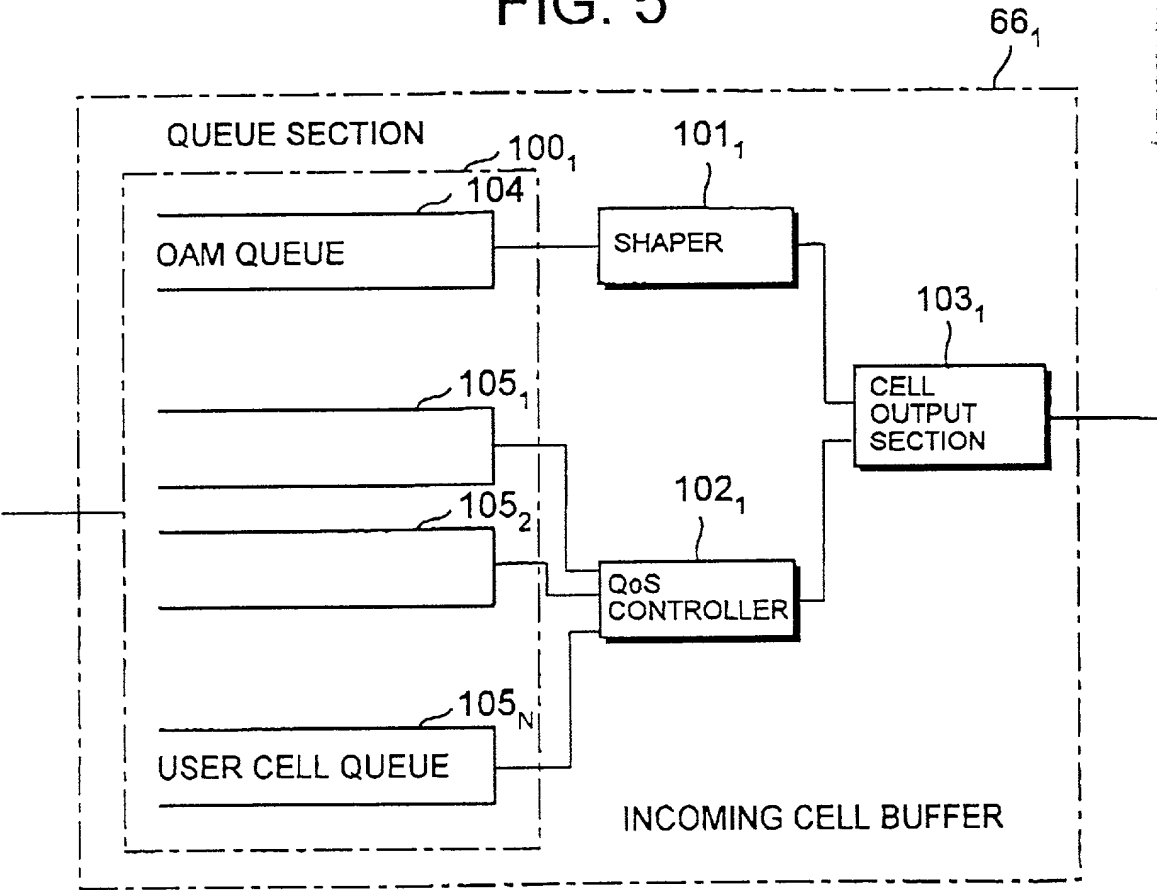
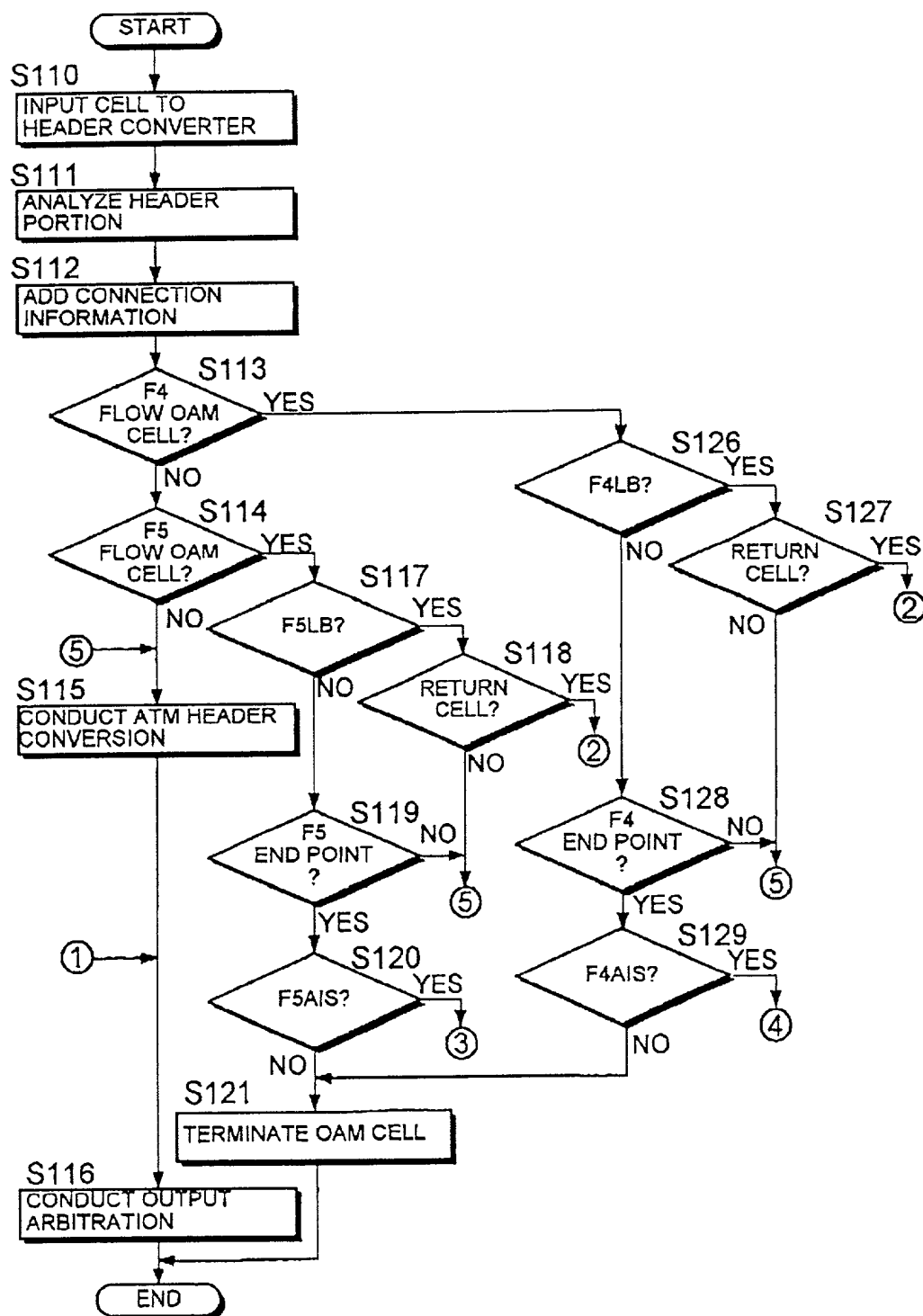


FIG. 6



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FIG. 7

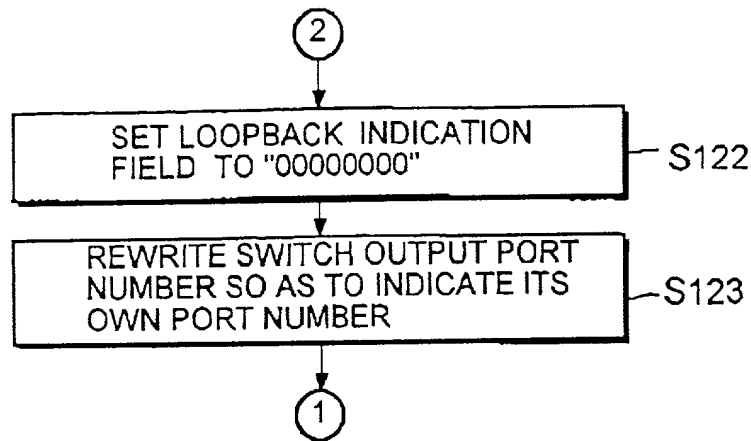


FIG. 8

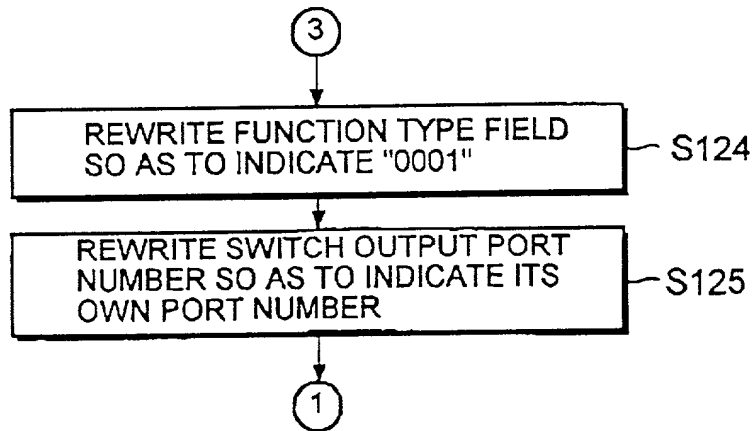


FIG. 9

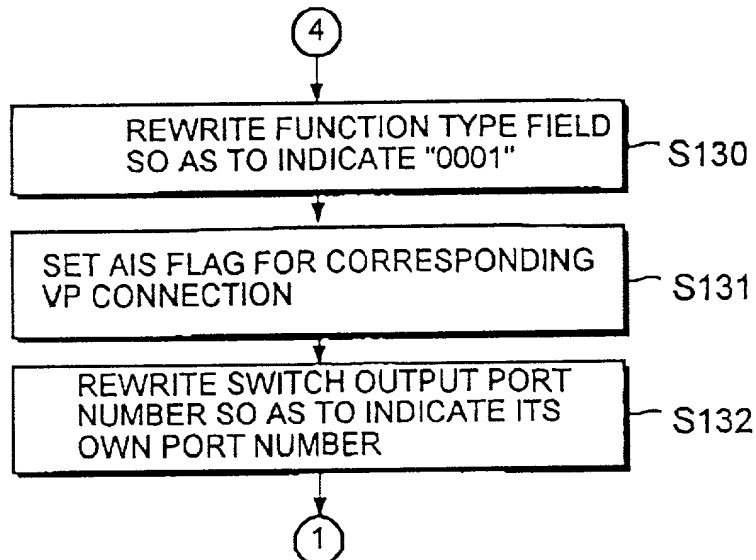


FIG. 10A is a block diagram of a first ATM cell structure. The cell structure includes an ATM cell header 150 and an ATM payload 151. FIG. 10B is a block diagram of a second ATM cell structure. The cell structure includes a new VPI/VCI field 153, an output port field 154, an ATM cell header 150, and an ATM payload 151. FIG. 10C is a block diagram of a third ATM cell structure. The cell structure includes an output port field 154, an ATM cell header 150, and an ATM payload 151.

FIG. 10A

FIG. 10B

FIG. 10C

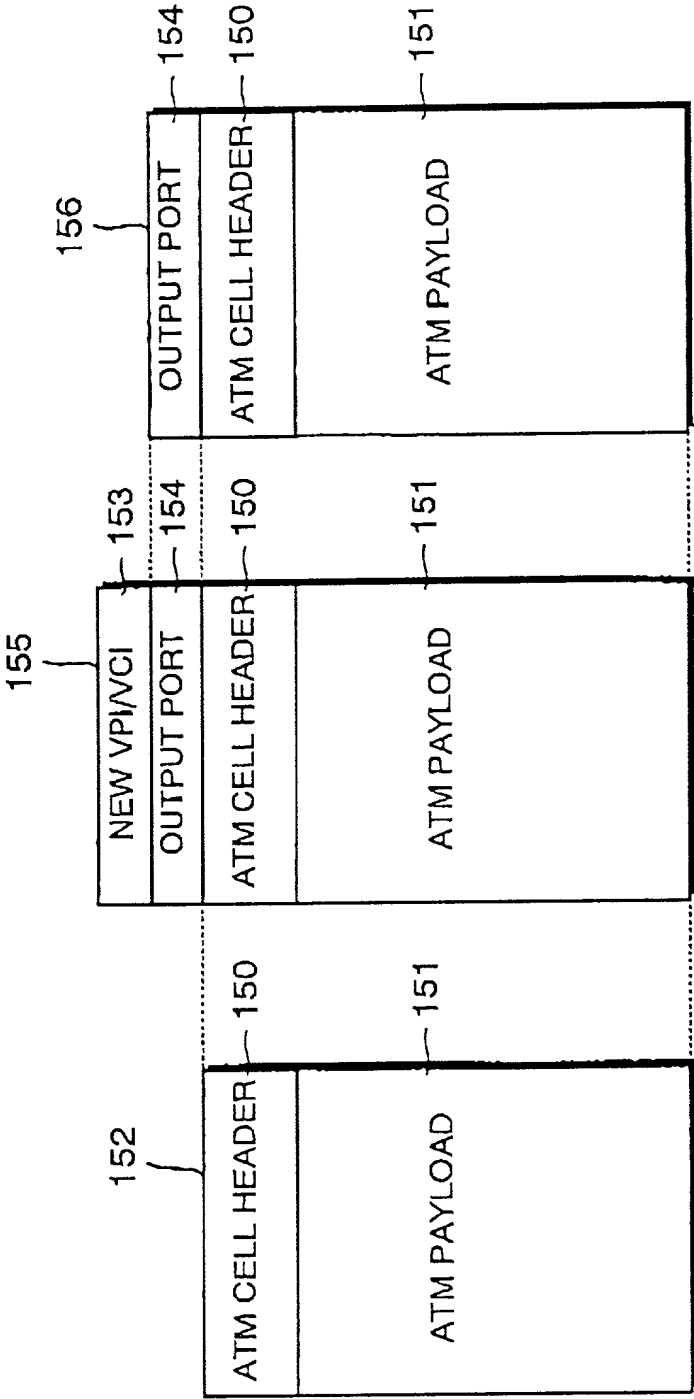


FIG. 11

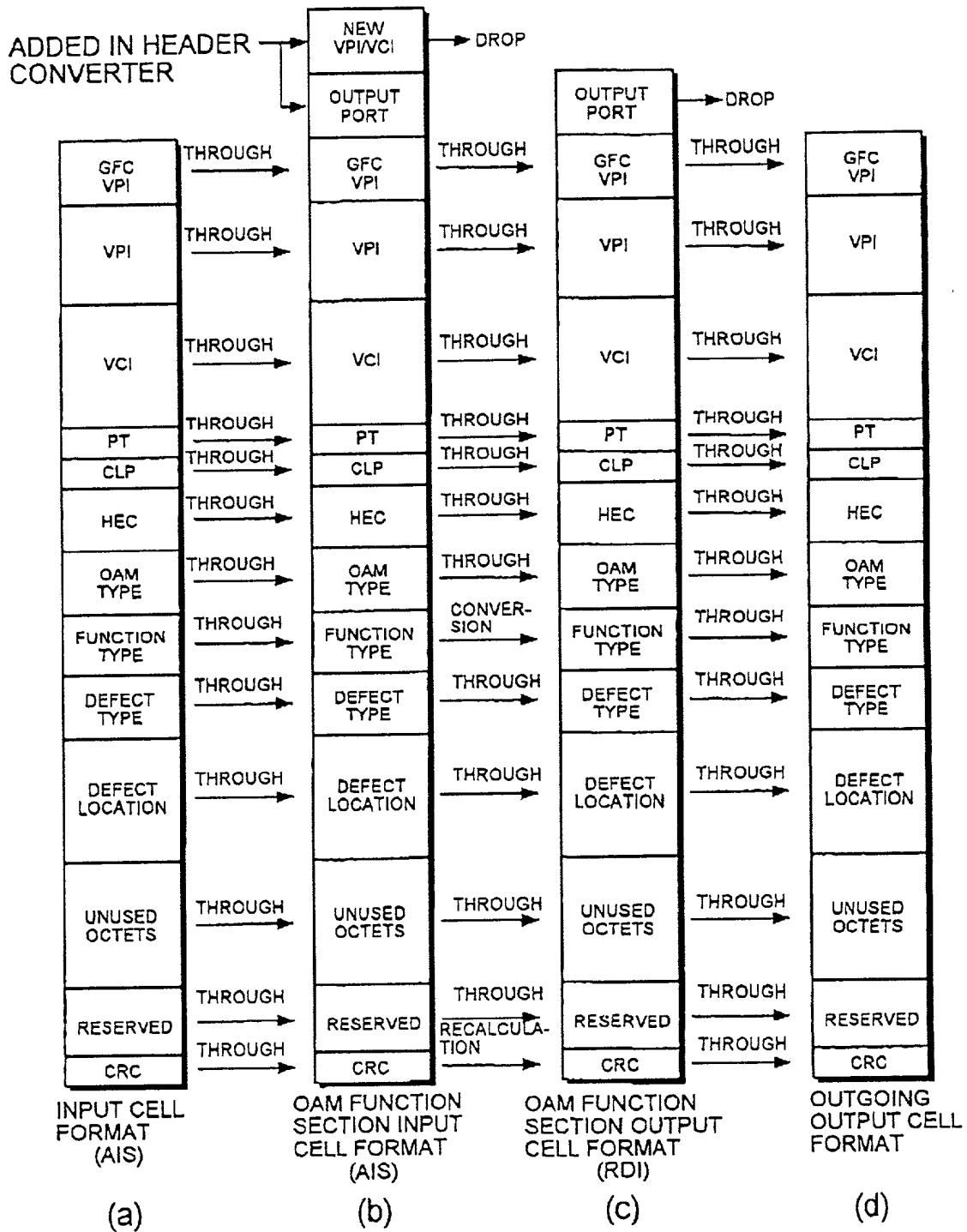


FIG. 12

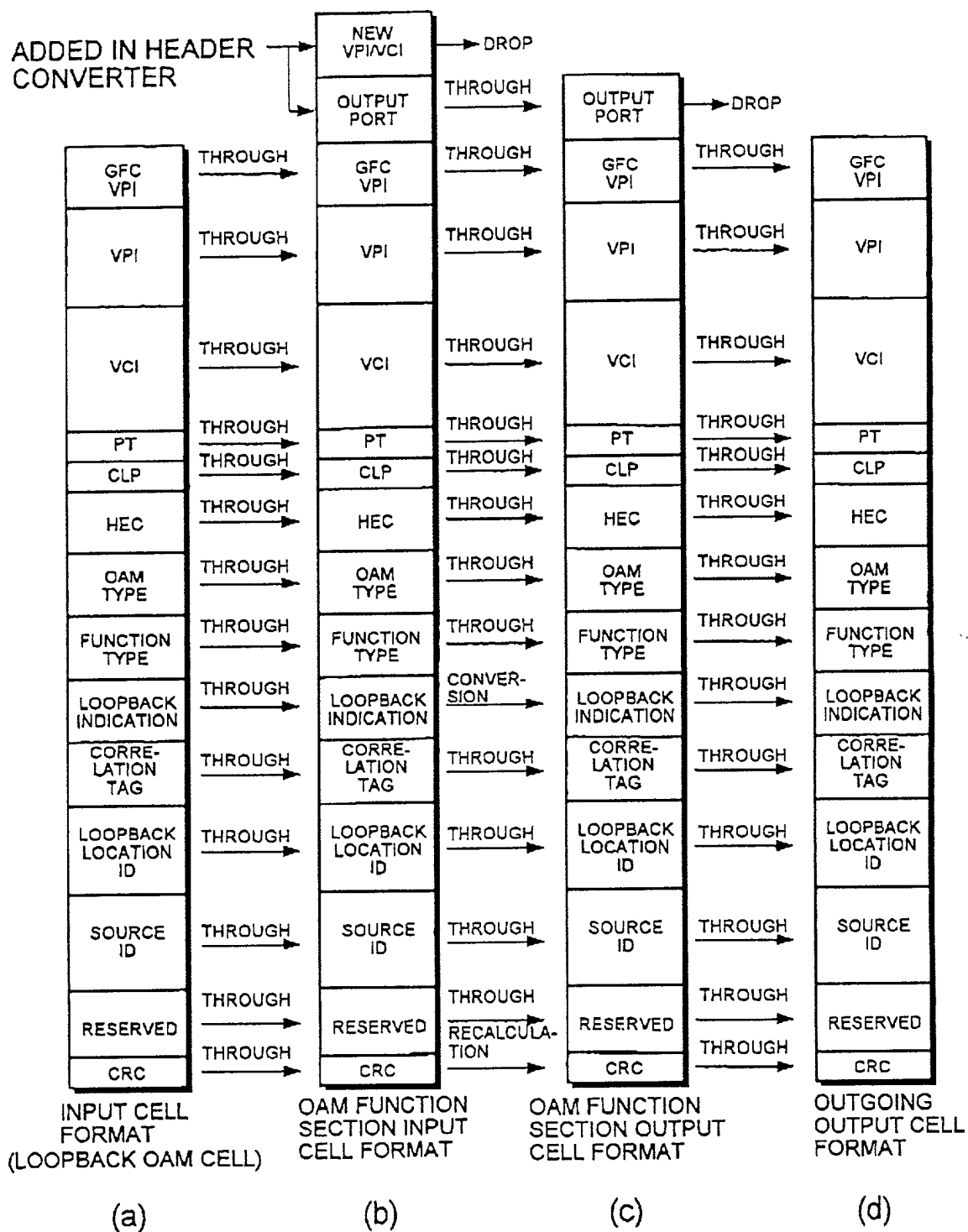




FIG. 13

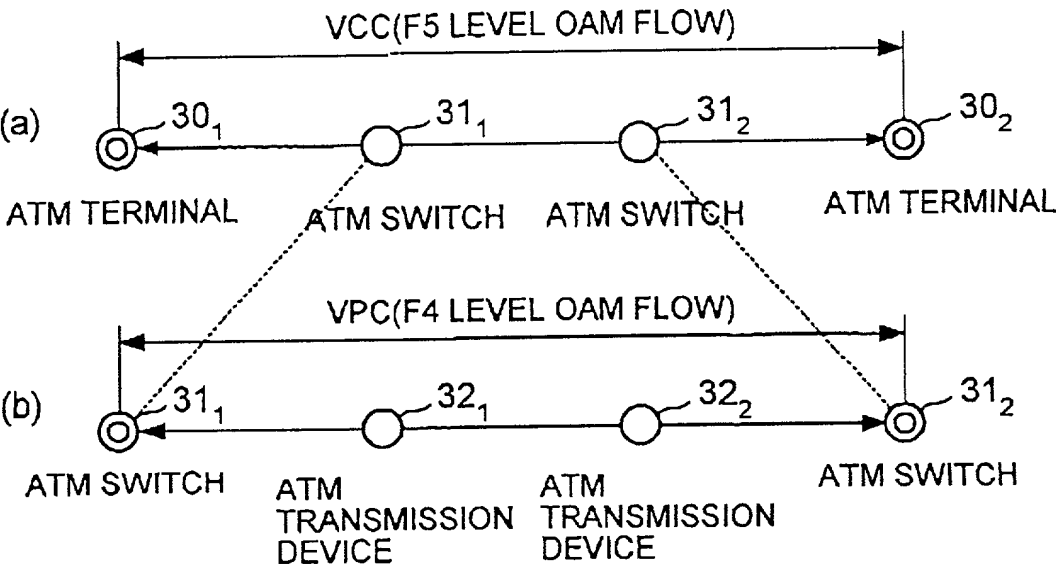


FIG. 14

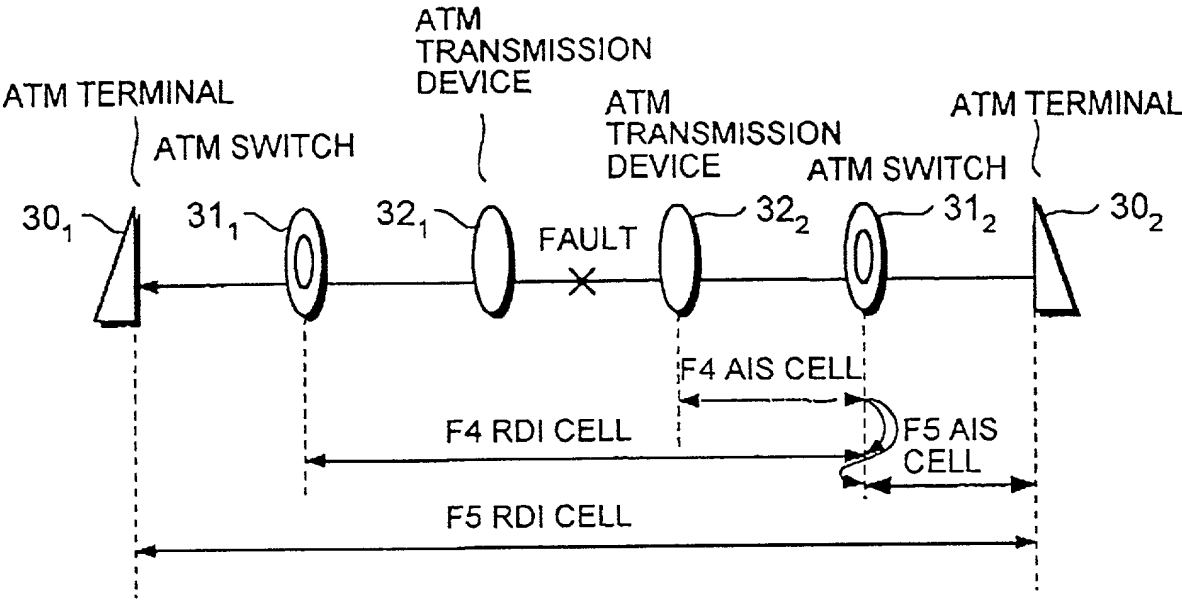


FIG. 15

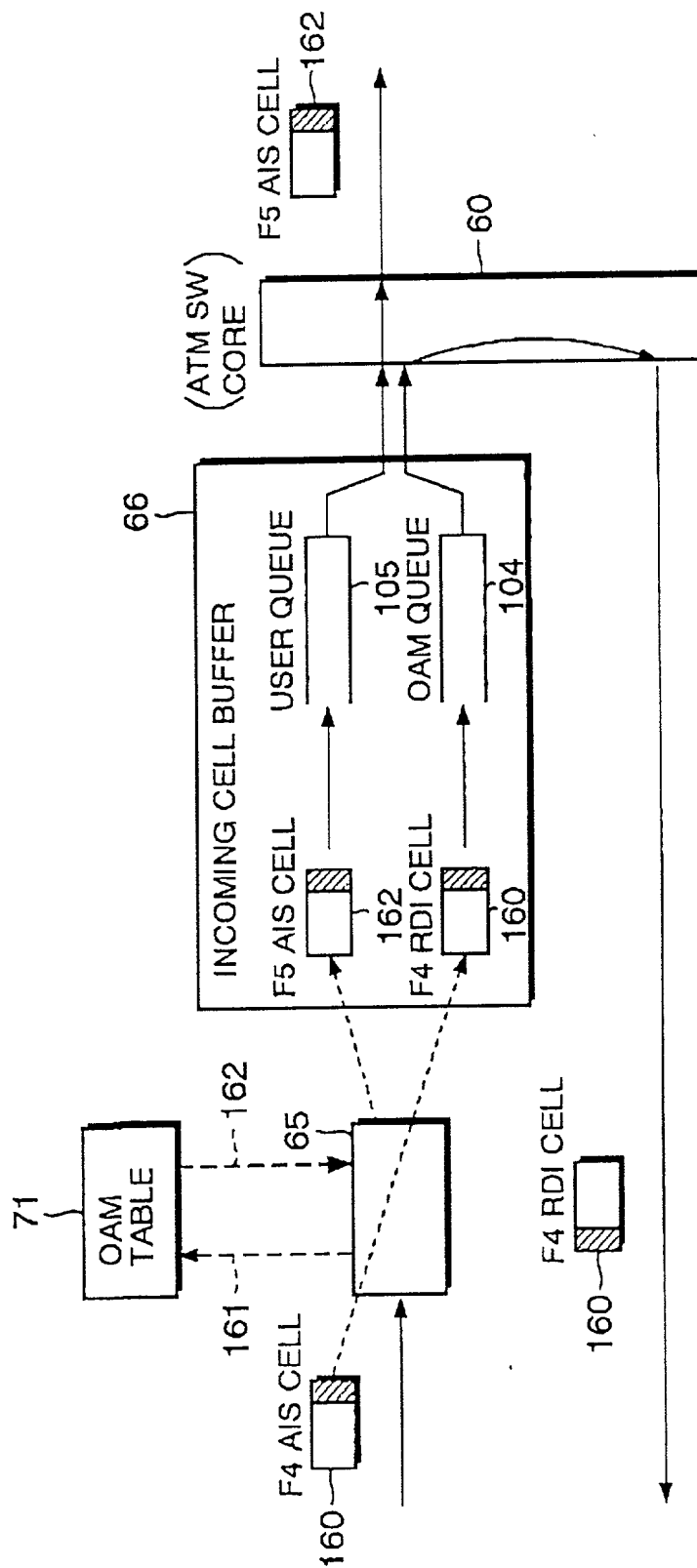


FIG. 16

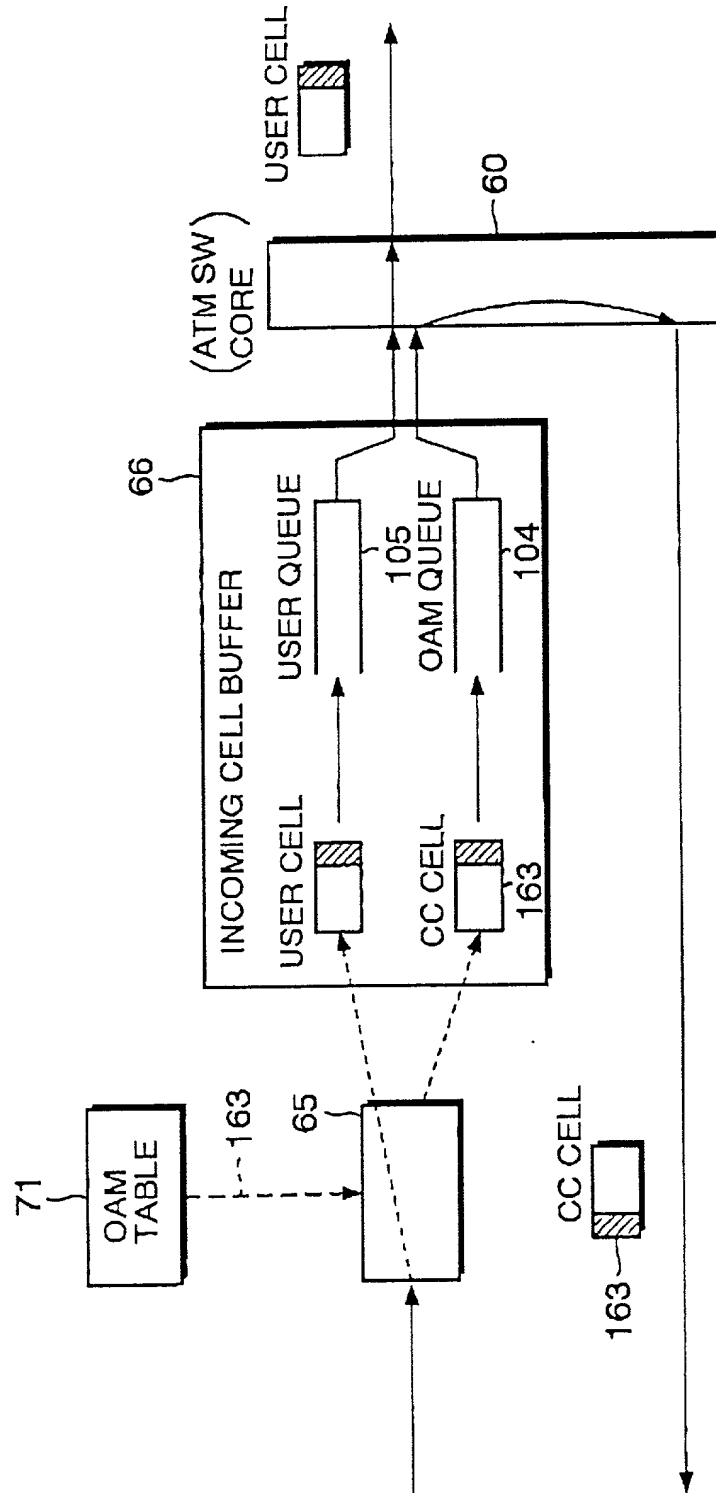


FIG. 17

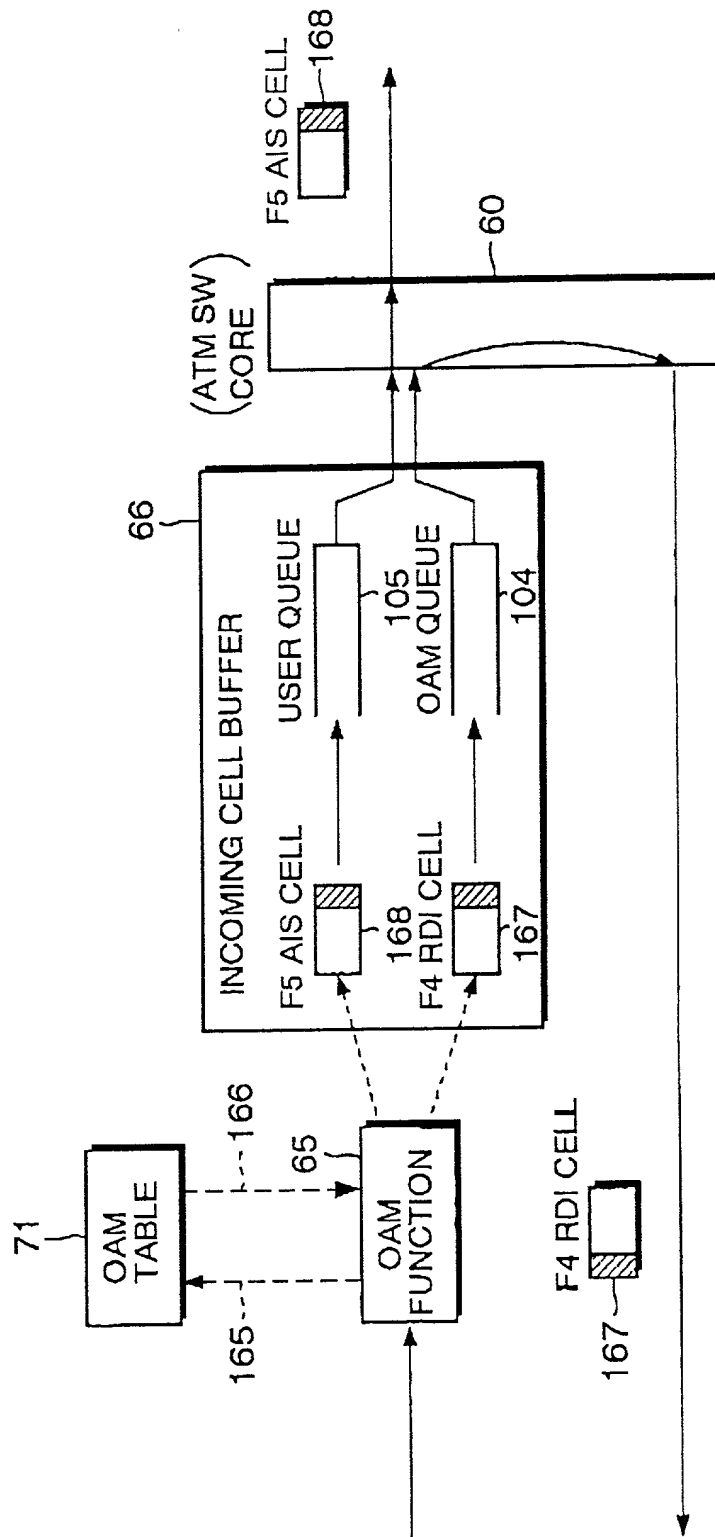


FIG. 18

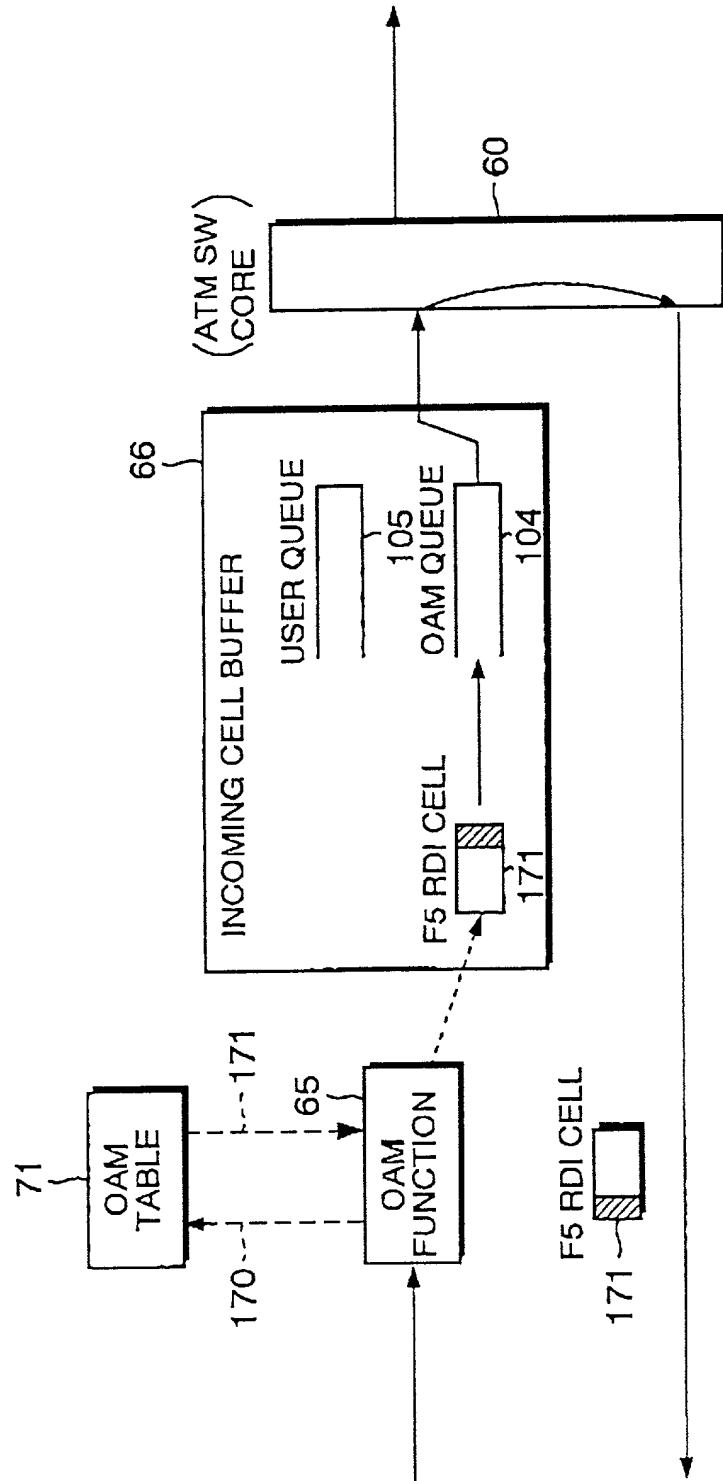


FIG. 19

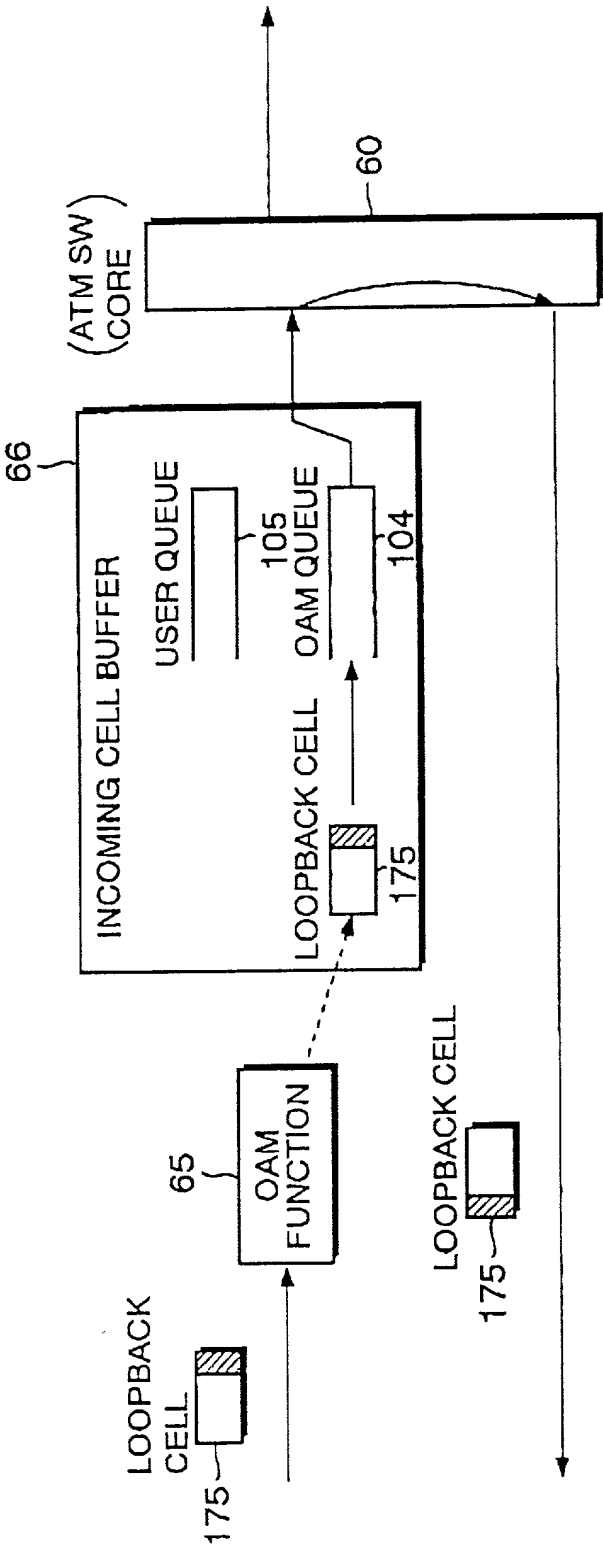


FIG. 20

OAM TYPE	CODE	FUNCTION TYPE	CODE
FAULT MANAGEMENT	0001	AIS	0000
		RDI	0001
		CONTINUITY CHECK	0100
		LOOPBACK	1000
PERFORMANCE MANAGEMENT	0010	FORWARD MONITORING	0000
		BACKWARD MONITORING	0001
ACTIVATION/ DEACTIVATION	1000	FORWARD MONITORING AND THE ASSOCIATED BACKWARD REPORTING	0000
		CONTINUITY CHECK	0001
		FORWARD MONITORING	0010
SYSTEM MANAGEMENT	1111	UNDEFINED	UNDEFINED

FIG. 21

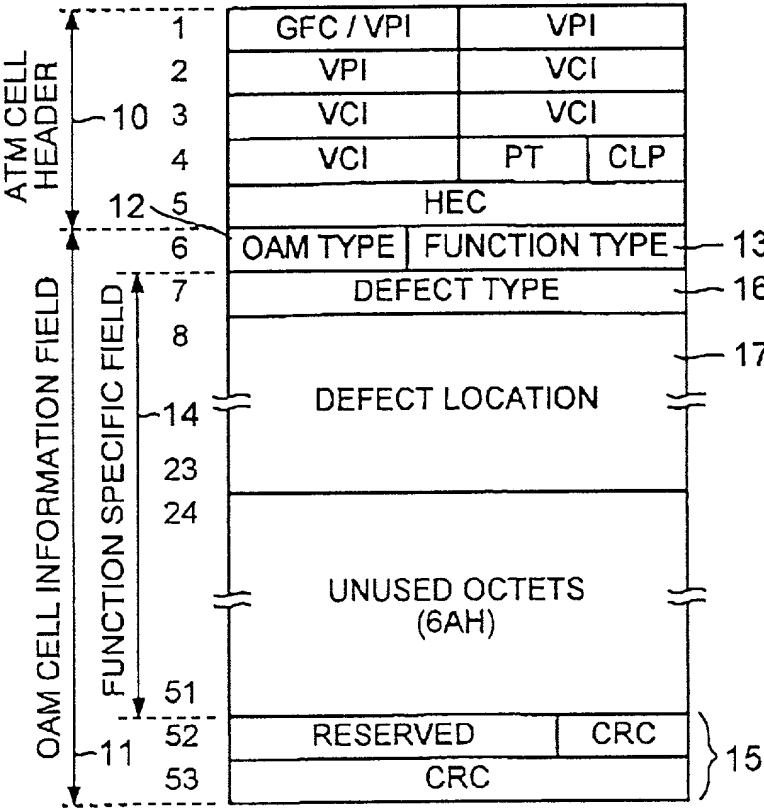


FIG. 22

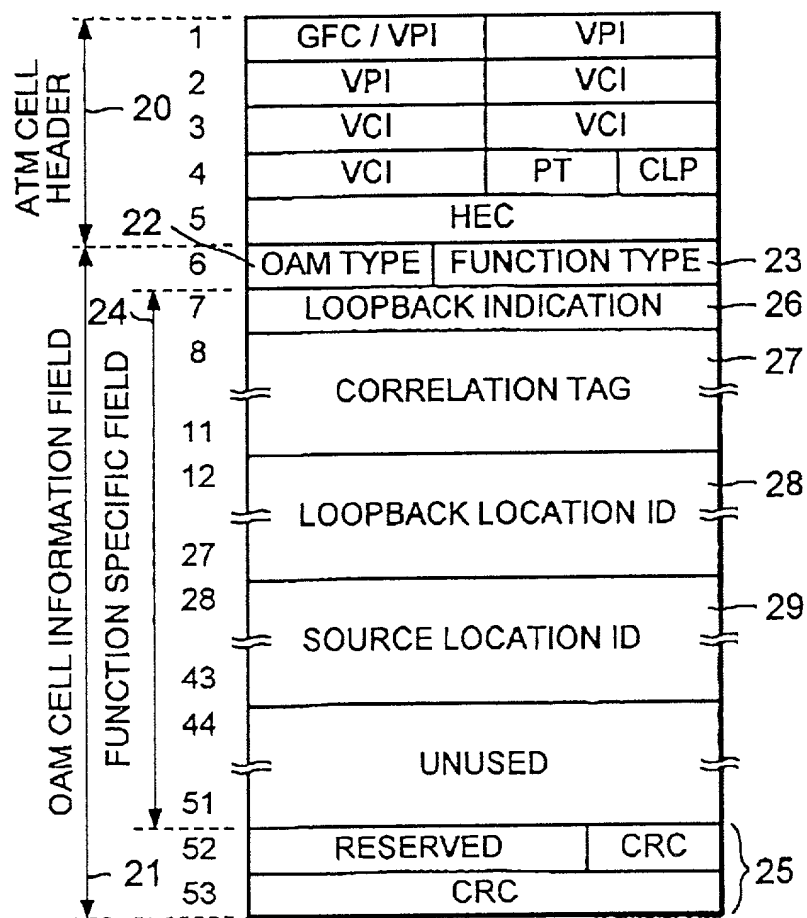




FIG. 23 (PRIOR ART)

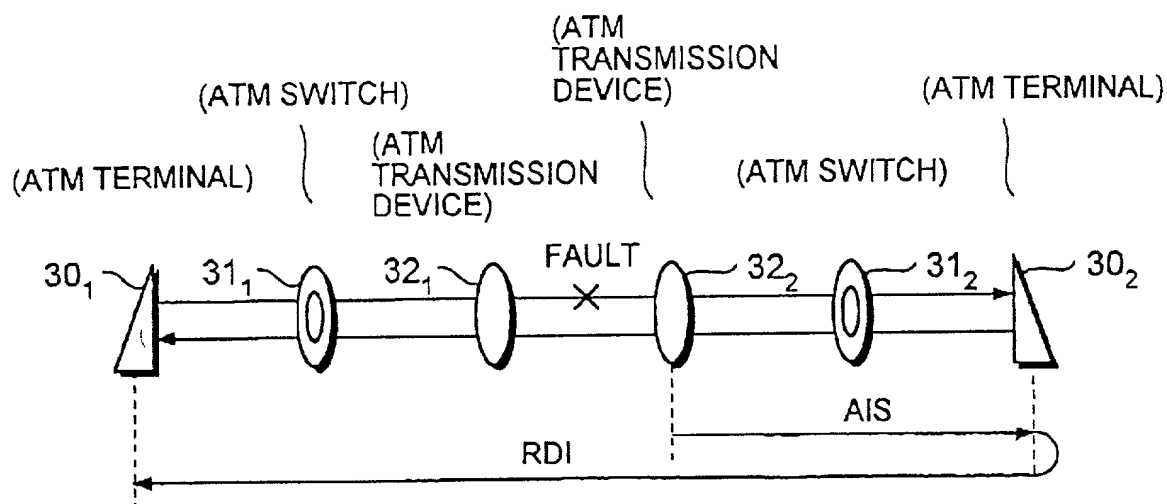


FIG. 24 (PRIOR ART)

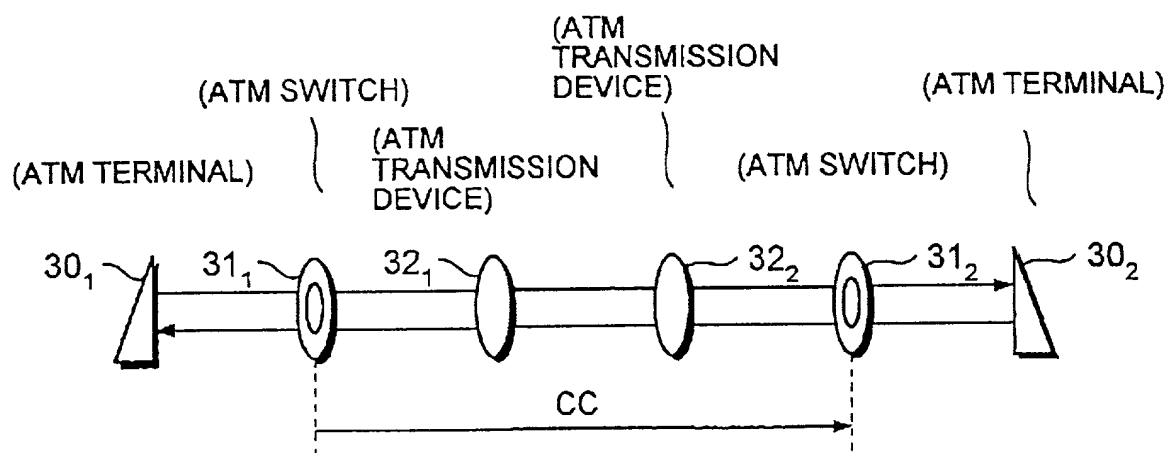


FIG. 25 (PRIOR ART)

